

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

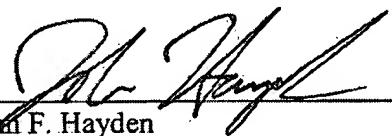
Applicant : Shunpei Yamazaki et al. Art Unit : 2629  
Serial No. : 09/504,235 Examiner : David L. Lewis  
Filed : February 15, 2000 Conf. No. : 7610  
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

**MAIL STOP RCE**  
Commissioner for Patents  
P.O. Box 1450  
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**INFORMATION DISCLOSURE STATEMENT**

Applicants request consideration of the reference listed on the attached PTO-1449 form.  
Under 37 C.F.R. § 1.98 (a)(2)(ii), a copy of the listed U.S. patent can be provided upon request.  
This filing is being made with the filing of a Request for Continued Examination. No fee  
is required.

Respectfully submitted,

  
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Date: September 24, 2007  
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